

# E32 SERIES

## Hex-Brick DC/DC Converter

230W / 25A · 36-75V or 18-36V Input · Patented Buck-Reset Forward  
 > 94% Peak Efficiency · 230 W/in<sup>3</sup> · 4.80 x 10<sup>6</sup> hrs MTBF



- OCP
- OVP
- OTP
- Remote ON/OFF
- Voltage Trim ±10%
- Remote Sense
- 60°C to +130°C
- 100 Bar Pressure
- RoHS
- CE Approved

**230W**

MAX OUTPUT

**25A**

MAX CURRENT

**>94%**

PEAK EFF.

**230 W/in<sup>3</sup>**

POWER DENSITY

**4.80M hrs**

MTBF

**480 kHz**

SWITCH FREQ

### PRODUCT OVERVIEW

The E32 Series is the industry-first Hex-Brick isolated DC/DC converter providing 5V/25A, 12V/10A and 24V/5A from 18-36V or 36-75V input ranges. Operating at -60°C to +130°C, the E32 is engineered for the most demanding environments: deep-water probes (1 mBar to 100 Bar), high-altitude instruments, defense systems, and harsh industrial equipment where conventional DC/DC modules cannot survive. The patented Buck-Reset Forward topology with Partial-Resonant-Synchronous-Rectifier stage at 480 kHz achieves 94% efficiency and 230 W/in<sup>3</sup>. A 60 nS ultra-fast current limiting circuit eliminates Short-Circuit-Current-Runaway for non-Ohmic loads. Wide-band Droop Current Sharing allows direct parallel connection without a current share bus.

### PART NUMBER SYSTEM

<b>E32</b> <small>Series</small>	<b>24 / 48</b> <small>Input Voltage</small>	<b>050 / 120 / 240</b> <small>Output Voltage</small>	<b>P / N</b> <small>Enable Logic</small>	<b>XYZ</b> <small>Suffix</small>
-------------------------------------	--	---	---	-------------------------------------

Input: 24 = 18V~36V | 48 = 36V~75V    Output: 050 = 5.0V | 120 = 12.0V | 240 = 24.0V

Enable: P = Positive Logic (+3V-6.5V = ON) | N = Negative Logic (0V-1V = ON)

*Note: A factory-issued Part Code is required in addition to the model number for all E32 orders.*

### TARGET APPLICATIONS

<b>Deep Water Probes</b> 1 mBar – 100 Bar	<b>High Altitude Instruments</b> -60°C Operation	<b>Defense &amp; Military</b> Harsh Vibration
<b>Industrial Extreme</b> Sealed Metal Case	<b>Parallel Power Systems</b> Droop Current Share	<b>Non-Ohmic Loads</b> 60 nS Ultra-fast OCP

### OUTPUT CAPACITIVE LOAD & SHORT CIRCUIT LIMITS

Part Number	Max Cap Load	Pre-bias VB	Reverse IB	Short Circ IS
E3224050a / E3248050a	<10,000 uF @ 200 mOhm	<4.75V	<100mA	<50A @ 2mOhm
E3224120a / E3248120a	<1,000 uF @ 1,200 mOhm	<11.4V	<100mA	<25A @ 2mOhm
E3224240a / E3248240a	<330 uF @ 4,800 mOhm	<22.8V	<100mA	<10A @ 2mOhm

### DROOP CURRENT SHARING — KEY FEATURES

Feature	E32 Droop Current Sharing
<b>No Share Bus</b>	Direct parallel output connection — no noise-sensitive current share bus
<b>Bandwidth</b>	Droop loop BW equals voltage loop — responds to 2.5A/uS load transients
<b>Step Load</b>	0A → 20A step at 2.5A/uS: settling within 20uS, no overshoot
<b>Share Error</b>	< 5% current share error per module at 30A total output current
<b>Max Parallel</b>	Multiple E32 modules — e.g. 3x E3248120a = 360W @ 12V

INPUT	
48V Operating Range	<b>+36V to +75V DC</b>
24V Operating Range	<b>+18V to +36V DC</b>
48V Transient (100ms)	<b>100V Maximum</b>
24V Transient (100ms)	<b>50V Maximum</b>
48V Power-ON Threshold	<b>+34.0V to +36.0V</b>
24V Power-ON Threshold	<b>+17.0V to +18.0V</b>
48V Power-OFF Threshold	<b>+31.2V to +33.2V</b>
24V Power-OFF Threshold	<b>+15.6V to +16.6V</b>
Off-State Input Current	<b>6 mA Max (at Vnom)</b>
Latch-State Input Current	<b>8 mA Max (at Vnom)</b>
Reflected Ripple Current	<b>20 mA rms / 60 mA p-p</b>
Input Capacitance (48V)	<b>14.0 uF Max</b>
Input Capacitance (24V)	<b>20.0 uF Max</b>
Ripple Rejection (<1 kHz)	<b>-50 dB (Vnom, Full Load)</b>

REMOTE CONTROL	
Logic High — Enable	<b>+3.0V to +6.5V</b>
Logic Low — Disable	<b>0V to +1.0V</b>
PC Pin Voltage Range	<b>-0.5V to +12V DC</b>
PC Pin Input Current	<b>-0.5 mA to +1.5 mA</b>
Positive Logic (P)	<b>High = Output ON</b>
Negative Logic (N)	<b>Low = Output ON</b>

OUTPUT	
Voltage Accuracy	<b>+/-1.0% typical</b>
Line Regulation	<b>+/-0.2% (full range)</b>
Load Regulation	<b>+/-0.2% (10-100% load)</b>
Temperature Drift	<b>+/-0.03%/degC (-60 to +130)</b>
Output Tolerance Band	<b>+/-4% (all conditions)</b>
Ripple & Noise (20 MHz)	<b>3% p-p / 1% RMS of Vo</b>
Over-Voltage Protection	<b>115-130% Vo</b>
Over-Current Limit	<b>120-140% Io (at Vnom)</b>
Voltage Trim Range	<b>+/-10% Vo (Vnom, 10% load)</b>
Step Load (2.5 A/uS)	<b>+/-6% Vo / 500 uS</b>
Start-Up Delay	<b>20 ms / 250 ms (full load)</b>

GENERAL PARAMETERS	
Switching Frequency	<b>480 kHz typical</b>
MTBF (Bellcore TR-332 #6)	<b>4.80x10<sup>6</sup> hrs @ GB/25 degC</b>
OTP Trip Point	<b>130 degC (Tc) +/-5 degC</b>
Weight	<b>16 g</b>

MECHANICAL	
Dimensions (L x W x H)	<b>25.49 x 26.50 x 11.00 mm</b>
Base Plate	<b>Anodized aluminum alloy</b>
Mounting Inserts	<b>M2 stainless steel, 2 places</b>
Max Torque	<b>1.0 in-lb (0.1 Nm)</b>
Pin Material	<b>Copper alloy or Brass</b>
Pin Plating	<b>Golden over Nickel</b>
Pressure Rating	<b>1 mBar to 100 Bar</b>

ISOLATION VOLTAGE	
Input to Output	<b>2.0 kV Minimum</b>
Input to Case	<b>1.0 kV Minimum</b>
Output to Case	<b>1.0 kV Minimum</b>

ABSOLUTE MAXIMUM RATINGS	
Input Voltage 24V Models	<b>-0.5V to +40V DC</b>
Input Voltage 48V Models	<b>-0.5V to +80V DC</b>
Remote Control	<b>-0.5V to +12V DC</b>
Operating Temperature	<b>-60 degC to +130 degC</b>
Storage Temperature	<b>-60 degC to +155 degC</b>

**WARNING — Do Not Exceed Absolute Maximum Ratings**  
 Exceeding these limits may permanently damage the device and voids warranty. Always protect input with a fuse.

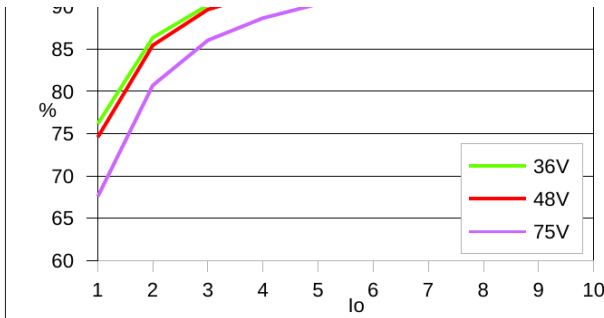
**48V Bus Series (36V ~ 75V Input)**

Part Number	Input	Vout	Iout	Pout	Eff.
E3248050a	36-75V	5.0V	25A	125W	92%
E3248120a	36-75V	12.0V	10A	120W	94%
E3248240a	36-75V	24.0V	5A	120W	93%

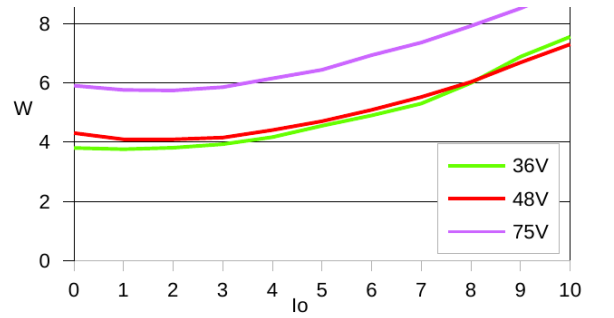
**24V Bus Series (18V ~ 36V Input)**

Part Number	Input	Vout	Iout	Pout	Eff.
E3224050a	18-36V	5.0V	25A	125W	91%
E3224120a	18-36V	12.0V	10A	120W	93%
E3224240a	18-36V	24.0V	5A	120W	92%

\* Contact OneTech Integration for special input/output configurations. Factory Part Code required for all orders.

**EFFICIENCY VS. LOAD CURRENT — E3248120A (VIN: 36V / 48V / 75V)**


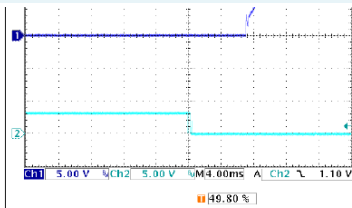
Efficiency plot of E3248120a



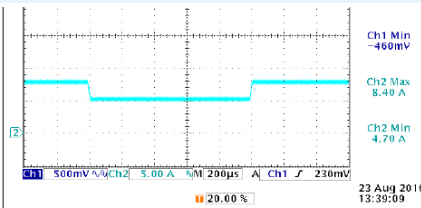
Power loss curves of E3248120a

**TYPICAL WAVEFORMS — E3248120A (VIN: 48V, LOAD: 10A)**

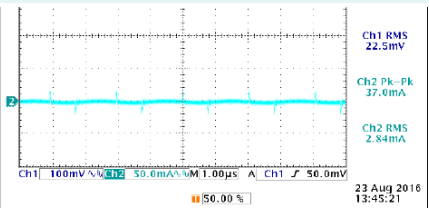
Start-up Waveform · Transient Response (5A/7.5A @ 2.5A/μS) · Input/Output Ripple (L<sub>IN</sub>=10μH)



Start-up waveform of E3248120a (VIN: 48V, Load: 10A)



Transient response of E3248120a (VIN: 48V, Load: 5A/7.5A@2.5A/μS)


 Input/Output ripples of E3248120a (VIN: 48V, Load: 10A, L<sub>IN</sub>=10μH)

**KEY PERFORMANCE DATA — E3248120a (48V Input, 12V/10A Output, Representative)**

Peak Efficiency	>94% @ 48V Input, 6A Load	Step Load Transient	+/-6% Vo within 500 uS
Efficiency @ Full Load	~92.5% @ 48V, 10A	Transient Recovery	<500 uS to +/-1% Vo band
Power Loss @ Full Load	~7.4W (92.5% efficiency)	Input Ripple Rejection	-50 dB at <1 kHz
Switching Frequency	480 kHz (typical)	Output Ripple (20MHz)	<3% pk-pk / <1% RMS of Vo
Start-up Time	20mS (to 90% Vo) / 250mS (to Vnom)	Droop Share Settling	<20 uS for 0A→20A step

**Model Number: E3224050a — 18-36V Input · 5.0V/25A Output · 450 kHz**
**MODEL PARAMETERS — ALL SPECIFICATIONS AT NOMINAL INPUT, FULL LOAD, 25°C UNLESS OTHERWISE NOTED**

Conversion Efficiency	See efficiency plots below	Ripple & Noise (20MHz)	3% (1%) $V_o$ peak-peak (RMS)
Switching Frequency	450 kHz (typical)	Over Voltage Protection	115-130% $V_o$ @ $V_{nom}$ , 10% Load
Voltage Accuracy	+/-1.0% typical	Output Current Limits	120-140% @ $V_{nom}$
Line Regulation	+/-0.2% (full input range)	Voltage Trim	+/-10% @ $V_{nom}$ , 10% Load
Load Regulation	+/-0.2% (10-100%, sensing pins connected)	Input Ripple Rejection	-50 dB (<1 kHz, $V_{nom}$ , Full Load)
Temperature Drift	+/-0.03%/degC (-60 to +130 degC)	Step Load (2.5A/uS)	+/-6% $V_o$ / 500uS (50-75% Load)
Output Tolerance Band	+/-4% (all conditions)	Start-Up Delay Time	20mS / 250mS @ $V_{nom}$ , Full Load

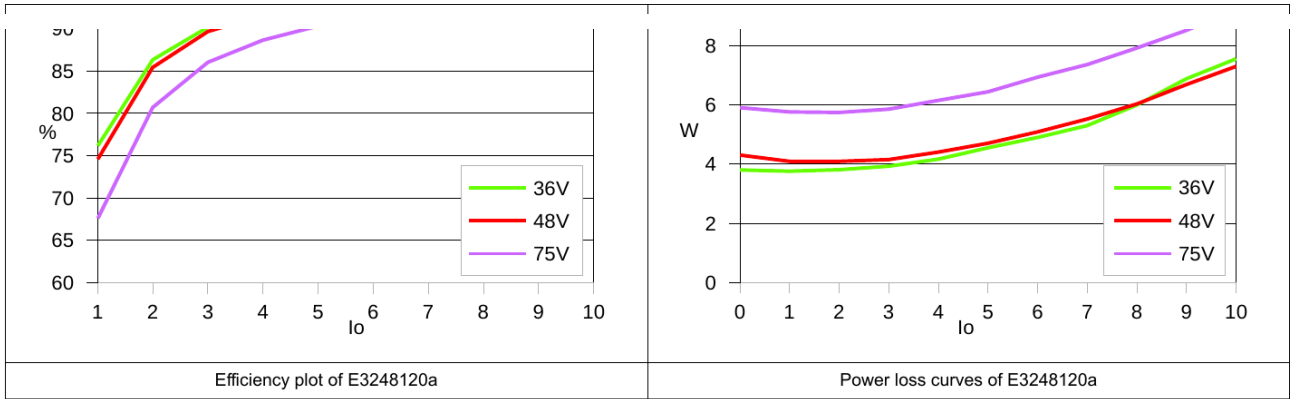
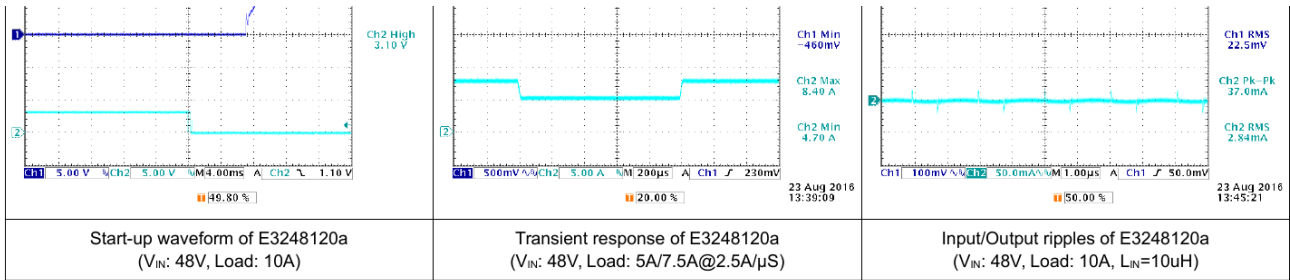
**APPLICATION NOTES**

**Input** E3224050a operates from 18-36V DC. Fuse each input individually. External input capacitor recommended to eliminate source oscillation.

**Output** Rated 5.0V/25A. Trim +/-10% via TRIM pin resistor to -S or +S. R-S resistor configures V-BUS compensation and droop sharing.

**Thermal** Operating range -60 degC to +130 degC. Forced-air and cold-plate cooling both supported. Double-side M2 screw attachment for secure mounting.

**OCP** 60 nS ultra-fast current limit eliminates Short-Circuit-Current-Runaway. Effective for capacitive, inductive, and non-Ohmic load applications.

**TYPICAL WAVES AND CURVES — E3248120A (VIN: 48V, LOAD: 10A, REPRESENTATIVE)**


**Model Number: E3224120a — 18-36V Input · 12.0V/10A Output · 480 kHz**

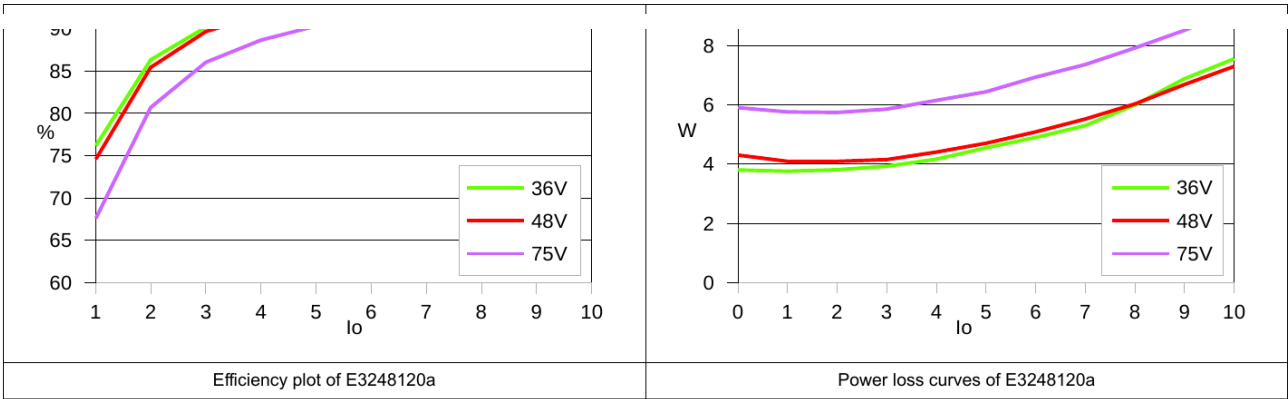
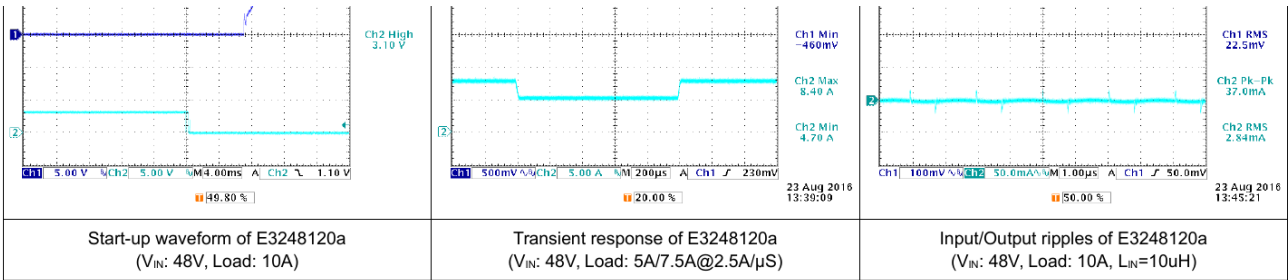
**MODEL PARAMETERS — ALL SPECIFICATIONS AT NOMINAL INPUT, FULL LOAD, 25°C UNLESS OTHERWISE NOTED**

Conversion Efficiency	See efficiency plots below	Ripple & Noise (20MHz)	3% (1%) $V_o$ peak-peak (RMS)
Switching Frequency	480 kHz (typical)	Over Voltage Protection	115-130% $V_o$ @ $V_{nom}$ , 10% Load
Voltage Accuracy	+/-1.0% typical	Output Current Limits	120-140% @ $V_{nom}$
Line Regulation	+/-0.2% (full input range)	Voltage Trim	+/-10% @ $V_{nom}$ , 10% Load
Load Regulation	+/-0.2% (10-100%, sensing pins connected)	Input Ripple Rejection	-50 dB (<1 kHz, $V_{nom}$ , Full Load)
Temperature Drift	+/-0.03%/degC (-60 to +130 degC)	Step Load (2.5A/uS)	+/-6% $V_o$ / 500uS (50-75% Load)
Output Tolerance Band	+/-4% (all conditions)	Start-Up Delay Time	20mS / 250mS @ $V_{nom}$ , Full Load

**APPLICATION NOTES**

- Input** E3224120a operates from 18-36V DC. Fuse each input individually. External input capacitor recommended to eliminate source oscillation.
- Output** Rated 12.0V/10A. Trim +/-10% via TRIM pin resistor to -S or +S. R-S resistor configures V-BUS compensation and droop sharing.
- Thermal** Operating range -60 degC to +130 degC. Forced-air and cold-plate cooling both supported. Double-side M2 screw attachment for secure mounting.
- OCP** 60 nS ultra-fast current limit eliminates Short-Circuit-Current-Runaway. Effective for capacitive, inductive, and non-Ohmic load applications.

**TYPICAL WAVES AND CURVES — E3248120A (VIN: 48V, LOAD: 10A, REPRESENTATIVE)**

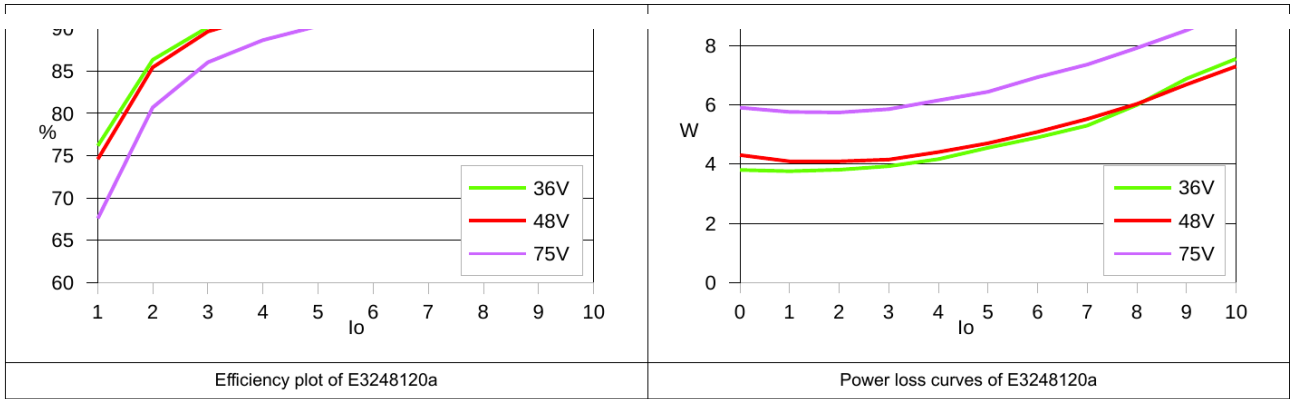
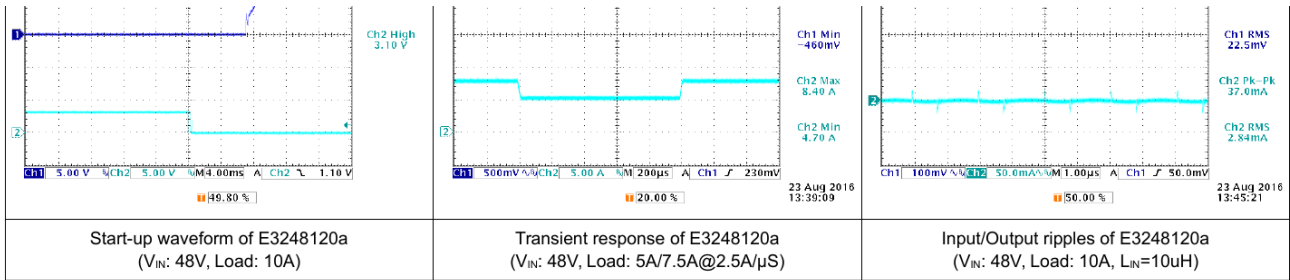


**Model Number: E3224240a — 18-36V Input · 24.0V/5A Output · 480 kHz**
**MODEL PARAMETERS — ALL SPECIFICATIONS AT NOMINAL INPUT, FULL LOAD, 25°C UNLESS OTHERWISE NOTED**

Conversion Efficiency	See efficiency plots below	Ripple & Noise (20MHz)	3% (1%) $V_o$ peak-peak (RMS)
Switching Frequency	480 kHz (typical)	Over Voltage Protection	115-130% $V_o$ @ $V_{nom}$ , 10% Load
Voltage Accuracy	+/-1.0% typical	Output Current Limits	120-140% @ $V_{nom}$
Line Regulation	+/-0.2% (full input range)	Voltage Trim	+/-10% @ $V_{nom}$ , 10% Load
Load Regulation	+/-0.2% (10-100%, sensing pins connected)	Input Ripple Rejection	-50 dB (<1 kHz, $V_{nom}$ , Full Load)
Temperature Drift	+/-0.03%/degC (-60 to +130 degC)	Step Load (2.5A/uS)	+/-6% $V_o$ / 500uS (50-75% Load)
Output Tolerance Band	+/-4% (all conditions)	Start-Up Delay Time	20mS / 250mS @ $V_{nom}$ , Full Load

**APPLICATION NOTES**

- Input** E3224240a operates from 18-36V DC. Fuse each input individually. External input capacitor recommended to eliminate source oscillation.
- Output** Rated 24.0V/5A. Trim +/-10% via TRIM pin resistor to -S or +S. R-S resistor configures V-BUS compensation and droop sharing.
- Thermal** Operating range -60 degC to +130 degC. Forced-air and cold-plate cooling both supported. Double-side M2 screw attachment for secure mounting.
- OCP** 60 nS ultra-fast current limit eliminates Short-Circuit-Current-Runaway. Effective for capacitive, inductive, and non-Ohmic load applications.

**TYPICAL WAVES AND CURVES — E3248120A (VIN: 48V, LOAD: 10A, REPRESENTATIVE)**


**Model Number: E3248050a — 36-75V Input · 5.0V/25A Output · 470 kHz**

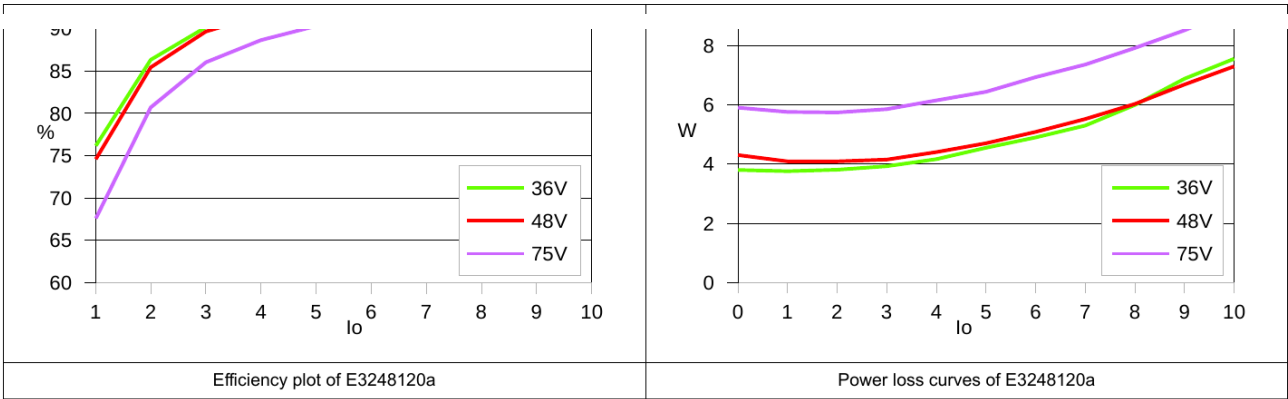
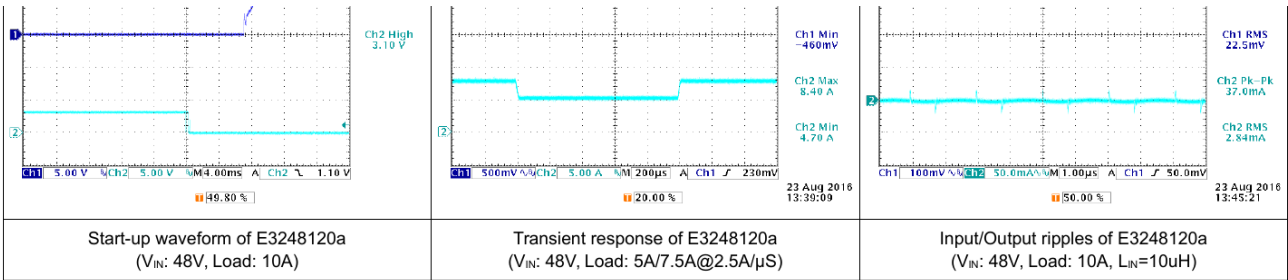
**MODEL PARAMETERS — ALL SPECIFICATIONS AT NOMINAL INPUT, FULL LOAD, 25°C UNLESS OTHERWISE NOTED**

Conversion Efficiency	See efficiency plots below	Ripple & Noise (20MHz)	3% (1%) $V_o$ peak-peak (RMS)
Switching Frequency	470 kHz (typical)	Over Voltage Protection	115-130% $V_o$ @ $V_{nom}$ , 10% Load
Voltage Accuracy	+/-1.0% typical	Output Current Limits	120-140% @ $V_{nom}$
Line Regulation	+/-0.2% (full input range)	Voltage Trim	+/-10% @ $V_{nom}$ , 10% Load
Load Regulation	+/-0.2% (10-100%, sensing pins connected)	Input Ripple Rejection	-50 dB (<1 kHz, $V_{nom}$ , Full Load)
Temperature Drift	+/-0.03%/degC (-60 to +130 degC)	Step Load (2.5A/uS)	+/-6% $V_o$ / 500uS (50-75% Load)
Output Tolerance Band	+/-4% (all conditions)	Start-Up Delay Time	20mS / 250mS @ $V_{nom}$ , Full Load

**APPLICATION NOTES**

- Input** E3248050a operates from 36-75V DC. Fuse each input individually. External input capacitor recommended to eliminate source oscillation.
- Output** Rated 5.0V/25A. Trim +/-10% via TRIM pin resistor to -S or +S. R-S resistor configures V-BUS compensation and droop sharing.
- Thermal** Operating range -60 degC to +130 degC. Forced-air and cold-plate cooling both supported. Double-side M2 screw attachment for secure mounting.
- OCP** 60 nS ultra-fast current limit eliminates Short-Circuit-Current-Runaway. Effective for capacitive, inductive, and non-Ohmic load applications.

**TYPICAL WAVES AND CURVES — E3248120A (VIN: 48V, LOAD: 10A, REPRESENTATIVE)**



**Model Number: E3248120a — 36-75V Input · 12.0V/10A Output · 480 kHz**

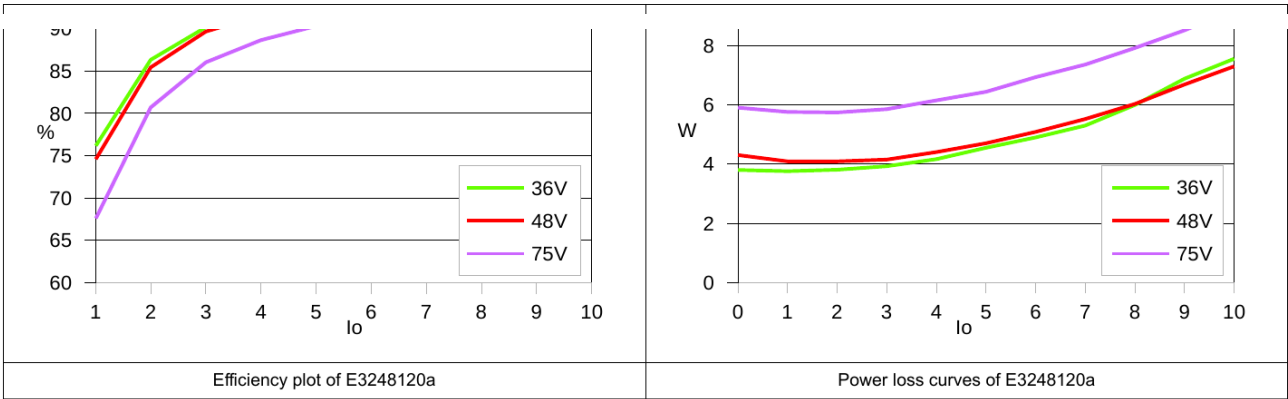
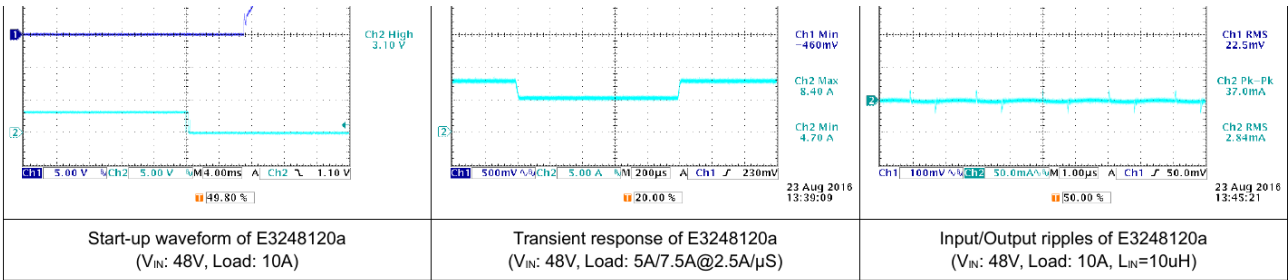
**MODEL PARAMETERS — ALL SPECIFICATIONS AT NOMINAL INPUT, FULL LOAD, 25°C UNLESS OTHERWISE NOTED**

Conversion Efficiency	See efficiency plots below	Ripple & Noise (20MHz)	3% (1%) $V_o$ peak-peak (RMS)
Switching Frequency	480 kHz (typical)	Over Voltage Protection	115-130% $V_o$ @ $V_{nom}$ , 10% Load
Voltage Accuracy	+/-1.0% typical	Output Current Limits	120-140% @ $V_{nom}$
Line Regulation	+/-0.2% (full input range)	Voltage Trim	+/-10% @ $V_{nom}$ , 10% Load
Load Regulation	+/-0.2% (10-100%, sensing pins connected)	Input Ripple Rejection	-50 dB (<1 kHz, $V_{nom}$ , Full Load)
Temperature Drift	+/-0.03%/degC (-60 to +130 degC)	Step Load (2.5A/uS)	+/-6% $V_o$ / 500uS (50-75% Load)
Output Tolerance Band	+/-4% (all conditions)	Start-Up Delay Time	20mS / 250mS @ $V_{nom}$ , Full Load

**APPLICATION NOTES**

- Input** E3248120a operates from 36-75V DC. Fuse each input individually. External input capacitor recommended to eliminate source oscillation.
- Output** Rated 12.0V/10A. Trim +/-10% via TRIM pin resistor to -S or +S. R-S resistor configures V-BUS compensation and droop sharing.
- Thermal** Operating range -60 degC to +130 degC. Forced-air and cold-plate cooling both supported. Double-side M2 screw attachment for secure mounting.
- OCP** 60 nS ultra-fast current limit eliminates Short-Circuit-Current-Runaway. Effective for capacitive, inductive, and non-Ohmic load applications.

**TYPICAL WAVES AND CURVES — E3248120A (VIN: 48V, LOAD: 10A, REPRESENTATIVE)**



**Model Number: E3248240a — 36-75V Input · 24.0V/5A Output · 480 kHz**
**MODEL PARAMETERS — ALL SPECIFICATIONS AT NOMINAL INPUT, FULL LOAD, 25°C UNLESS OTHERWISE NOTED**

Conversion Efficiency	See efficiency plots below	Ripple & Noise (20MHz)	3% (1%) $V_o$ peak-peak (RMS)
Switching Frequency	480 kHz (typical)	Over Voltage Protection	115-130% $V_o$ @ $V_{nom}$ , 10% Load
Voltage Accuracy	+/-1.0% typical	Output Current Limits	120-140% @ $V_{nom}$
Line Regulation	+/-0.2% (full input range)	Voltage Trim	+/-10% @ $V_{nom}$ , 10% Load
Load Regulation	+/-0.2% (10-100%, sensing pins connected)	Input Ripple Rejection	-50 dB (<1 kHz, $V_{nom}$ , Full Load)
Temperature Drift	+/-0.03%/degC (-60 to +130 degC)	Step Load (2.5A/uS)	+/-6% $V_o$ / 500uS (50-75% Load)
Output Tolerance Band	+/-4% (all conditions)	Start-Up Delay Time	20mS / 250mS @ $V_{nom}$ , Full Load

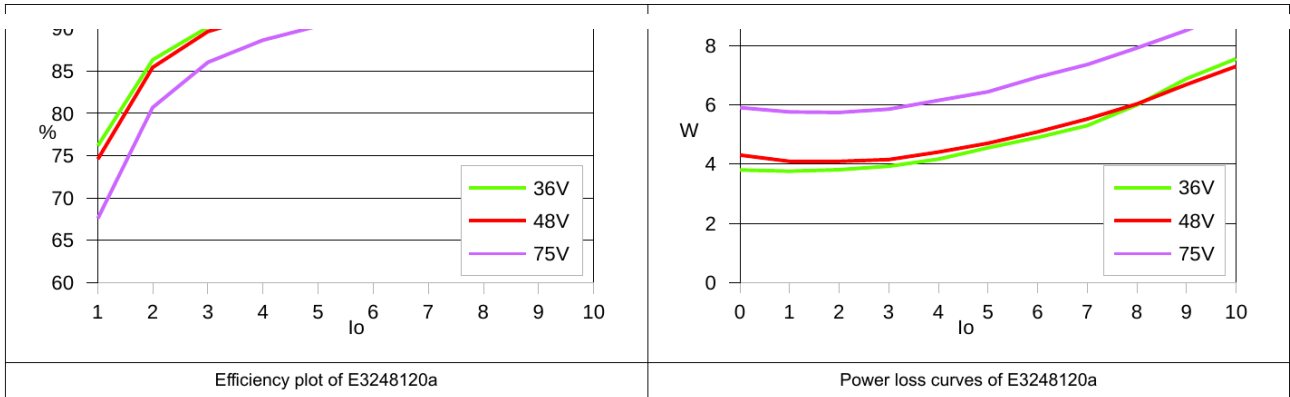
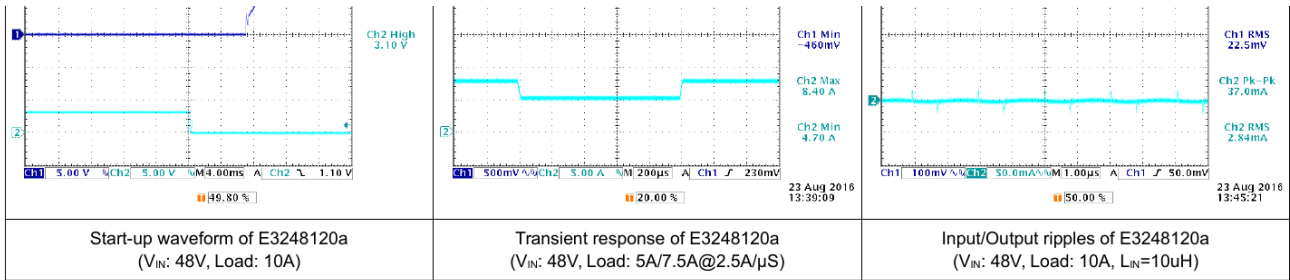
**APPLICATION NOTES**

**Input** E3248240a operates from 36-75V DC. Fuse each input individually. External input capacitor recommended to eliminate source oscillation.

**Output** Rated 24.0V/5A. Trim +/-10% via TRIM pin resistor to -S or +S. R-S resistor configures V-BUS compensation and droop sharing.

**Thermal** Operating range -60 degC to +130 degC. Forced-air and cold-plate cooling both supported. Double-side M2 screw attachment for secure mounting.

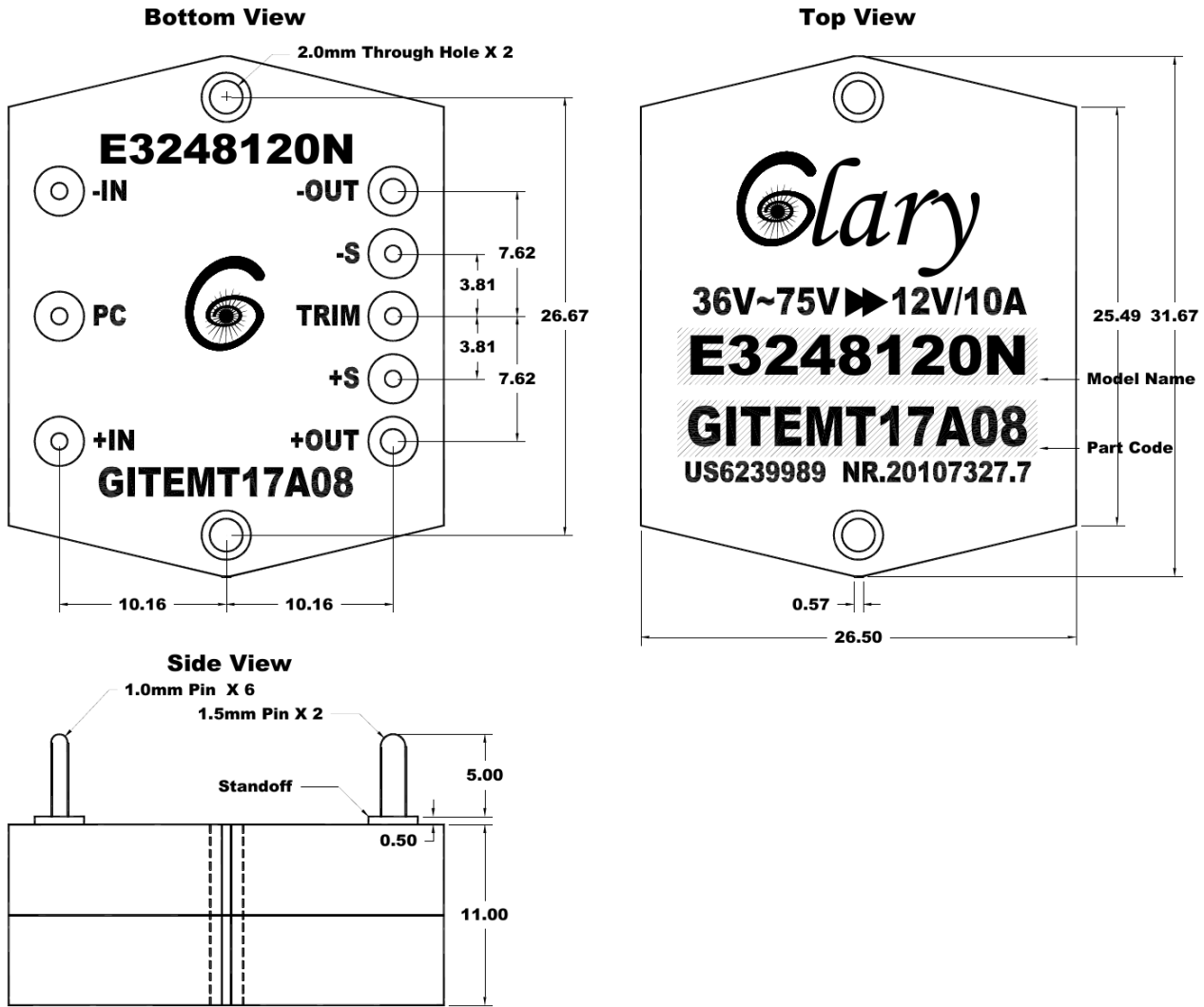
**OCP** 60 nS ultra-fast current limit eliminates Short-Circuit-Current-Runaway. Effective for capacitive, inductive, and non-Ohmic load applications.

**TYPICAL WAVES AND CURVES — E3248120A ( $V_{IN}$ : 48V, LOAD: 10A, REPRESENTATIVE)**


**MECHANICAL DRAWING & PIN CONNECTIONS**

All dimensions in mm. Tolerances: .x ±0.5 mm / .xx ±0.25 mm. Weight: 16g. Base plate: Anodized aluminum alloy. Mounting: M2 S/S, 2 places, Max torque: 1.0 in-lb (0.1 Nm)

**MECHANICAL DRAWING**



**Dimensions and Pin Connections**

Designation	Function Description	Pin #
-IN	Negative input	1
PC	Primary control: ON/OFF and Synchronization.	2
+IN	Positive input	3
+OUT	Positive output	4
+S	Positive remote sense	5
TRIM	Output voltage adjust	6
-S	Negative remote sense	7
-OUT	Negative output	8

**Dimensions:** mm

**Tolerances:** .x±0.5mm  
.xx±0.25mm

**Weight:** 16g / Metal enclosed

**Metallic case:** Anode oxide aluminum alloy

**Mounting inserts:** Stainless steel for M2

**Maximum torque:** 1.0 in-lb (0.1Nm)

**Pin material:** Copper alloy or Brass

**Pin plating:** Golden over Nickel

**ORDERING INFORMATION**

<b>E32</b> <small>Series</small>	<b>24/48</b> <small>Input</small>	<b>050/120/240</b> <small>Output</small>	<b>P / N</b> <small>Enable</small>	<b>XYZ</b> <small>Part Code</small>
-------------------------------------	--------------------------------------	---	---------------------------------------	--

Input: 24 = 18V~36V | 48 = 36V~75V    Output: 050 = 5.0V/25A | 120 = 12.0V/10A | 240 = 24.0V/5A

Enable: P = Positive Logic (+3V-6.5V = ON) | N = Negative Logic (0V-1V = ON)

**Example: E3248120N — 48V input · 12.0V/10A output · Negative enable logic · + factory Part Code**

Field	Code	Description	IMPORTANT NOTES
Series	<b>E32</b>	Hex-Brick DC/DC Converter	! Input <b>MUST</b> be protected by fuses or overcurrent protection device.
Input Voltage	<b>24</b>	18V~36V input (24V bus)	i A factory Part Code is required — model number alone is insufficient.
	<b>48</b>	36V~75V input (48V bus)	i All specifications at nominal input, full load, 25 degC unless noted.
	<b>050</b>	5.0V / 25A	i Specifications subject to change without notice.
Output Voltage	<b>120</b>	12.0V / 10A	i Not for life-critical, nuclear, or hazardous system applications.
	<b>240</b>	24.0V / 5A	i Contact OneTech Integration for custom configurations.
	<b>N</b>	Negative Logic (0V-1V = ON)	i Do not cut pins, remove potting, or modify without written consent.
Enable Logic	<b>P</b>	Positive Logic (+3V-6.5V = ON)	i Remote sense compensates max 0.5V. Exceeding may cause instability.
	<b>XYZ</b>	Factory-issued — required for all orders	i Trim range beyond +/-10% available — contact OneTech Integration.

**PCB LAYOUT — PAD SPECIFICATIONS**

Pin Type	Hole	Pad	Via Requirement
<b>1.0mm Pins (x6)</b>	1.25mm	2.0mm min	4-8 x 0.5mm vias per hole
<b>1.5mm Pins (x2)</b>	1.80mm	3.0mm min	4-8 x 0.5mm vias per hole

**SOLDERING GUIDELINES**

Method	Specification
<b>Hand Solder</b>	425 degC iron, 70W. 3-6 sec per 1.0mm pin. 5-10 sec per 1.5mm pin.
<b>Wave Solder</b>	Max solder pot 250 degC. Wave dwell time 3 seconds typical, 6 seconds max.
<b>De-moisture</b>	Bake at 85 degC / 12 hrs before soldering if MSL1 storage period.
<b>Removal</b>	Desolder using desoldering tool only. Never pry, force, or bend pins.
<b>Cleaning</b>	Compatible with most agents. Verify compatibility with silicone potting.
<b>Storage</b>	Store at 30 degC / 60%RH. Gold pin plating supports long-term storage.

**GENERAL APPLICATION NOTES**

Topic	Guidance
<b>Input Fuse</b>	Use slow-blow fuse; lowest rating allowing normal start-up sequence.
<b>Input Cap</b>	External capacitor recommended to reduce source impedance and provide transient current.
<b>Output Cap</b>	Low-ESR capacitor close to load handles high-frequency transient current.
<b>OVP Recovery</b>	To recover from OVP latch: cycle input power off/on or toggle PC pin.
<b>Parallel Use</b>	-S droop resistor must be configured identically on all paralleled modules.
<b>Trim Up</b>	Connect RU between TRIM and -S. Verify output stays below OVP setpoint.
<b>Trim Down</b>	Connect RD between TRIM and +S. Verify output meets minimum load regulation.